

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ANDREAS WICHERN

DE 000152

Serial No.

Filed: CONCURRENTLY

CIRCUIT ARRANGEMENT

Commissioner for Patents  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,  
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

3. (Amended) A circuit arrangement as claimed in claim 1,  
characterized in that the individual resonant circuits (10;  
20; 30) are essentially arranged in a planar way on an outer  
side, particularly on an outer surface area of the integrated  
circuit.

4. (Amended) A circuit arrangement as claimed in claim 1,  
characterized in that the individual resonant circuits (10;  
20; 30) are constituted by essentially concentric geometric  
structures each having at least one turn as an inductive  
element (12; 22; 32) and each having a capacitor as a  
capacitive element (14; 24; 34).

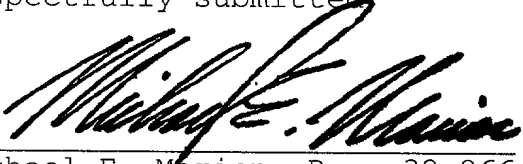
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rights he may have under the Doctrine of Equivalents.

Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted

By   
Michael E. Marion, Reg. 32,266  
Attorney  
(914) 333-9641

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## APPENDIX

3. (Amended) A circuit arrangement as claimed in claim 1 ~~or~~ 2, characterized in that the individual resonant circuits (10; 20; 30) are essentially arranged in a planar way on an outer side, particularly on an outer surface area of the integrated circuit.

4. (Amended) A circuit arrangement as claimed in claim 1 ~~any one of claims 1 to 3~~, characterized in that the individual resonant circuits (10; 20; 30) are constituted by essentially concentric geometric structures each having at least one turn as an inductive element (12; 22; 32) and each having a capacitor as a capacitive element (14; 24; 34).

6. (Amended) A circuit arrangement as claimed in claim 4 ~~or~~ 5, characterized in that the capacitive element (14; 24; 34) is arranged at the ends of a single turn.

7. (Amended) A circuit arrangement as claimed in claim 1 ~~any one of claims 1 to 6~~, characterized in that at least two inductive elements (12; 22; 32) each comprise one, preferably a plurality of turns which are substantially concentric and/or substantially parallel to each other.

8. (Amended) A circuit arrangement as claimed in claim 1 ~~any one of claims 1 to 7~~, characterized in that the capacitive elements (14; 24; 34) are arranged one after the other in essentially one direction (D).

9. (Amended) A circuit arrangement as claimed in claim 1 ~~any one of claims 1 to 8~~, characterized in that more than two resonant circuits (10; 20; 30) are magnetically fixedly coupled to each other.

10. (Amended) A circuit arrangement as claimed in claim 1 ~~any one of claims 1 to 9~~, characterized in that the resonant circuits (10; 20; 30) of the circuit arrangement (100) are arranged on the upper metallization plate (40) of the integrated circuit.

11. (Amended) An integrated circuit comprising at least a circuit arrangement (100) as claimed in claim 1 ~~any one of claims 1 to 10~~.

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